

Application Data Sheet

**Application Information**

Application Type:: Regular

Subject Matter:: Utility

Suggested Classification::

Suggested Group Art Unit::

CD-ROM or CD-R?:: None

Title:: Arrangement for Measuring Pressure on a Semiconductor Wafer and an Associated Method for Fabricating a Semiconductor Wafer

Request for Early Publication?:: No

Request for Non-Publication?:: Yes

Suggested Drawing Figure:: 53

Total Drawing Sheets:: 15

Small Entity:: No

Petition included?:: No

Secrecy Order in Parent Appl.?:: No

**Applicant Information**

Applicant Authority type:: Inventor

Primary Citizenship Country: US

Status:: Full Capacity

Given Name: Newell E.

Family Name:: Chiesl

City of Residence:: Colorado Springs

State if Province of Residence:: CO

Country of Residence:: USA  
Street of mailing address:: 5362 Nevada #311  
City of mailing address:: Colorado Springs  
State or Province of mailing address:: CO  
Postal or Zip Code of mailing address:: 80918

**Correspondence Information**

Correspondence Customer Number::



24319

PATENT TRADEMARK OFFICE

**Representative Information**

Representative Customer Number:



24319

PATENT TRADEMARK OFFICE

**Domestic Priority Information**

Application::	Continuity Type:	Parent Application::	Parent Filing Date::

**Assignee Information**

Assignee name:: LSI Logic Corporation  
Street of mailing address:: 1551 McCarthy Boulevard  
City of mailing address:: Milpitas  
State or Province of mailing address:: CA  
Country of mailing address:: USA  
Postal or Zip Code of mailing address:: 95035